

FIG. 1

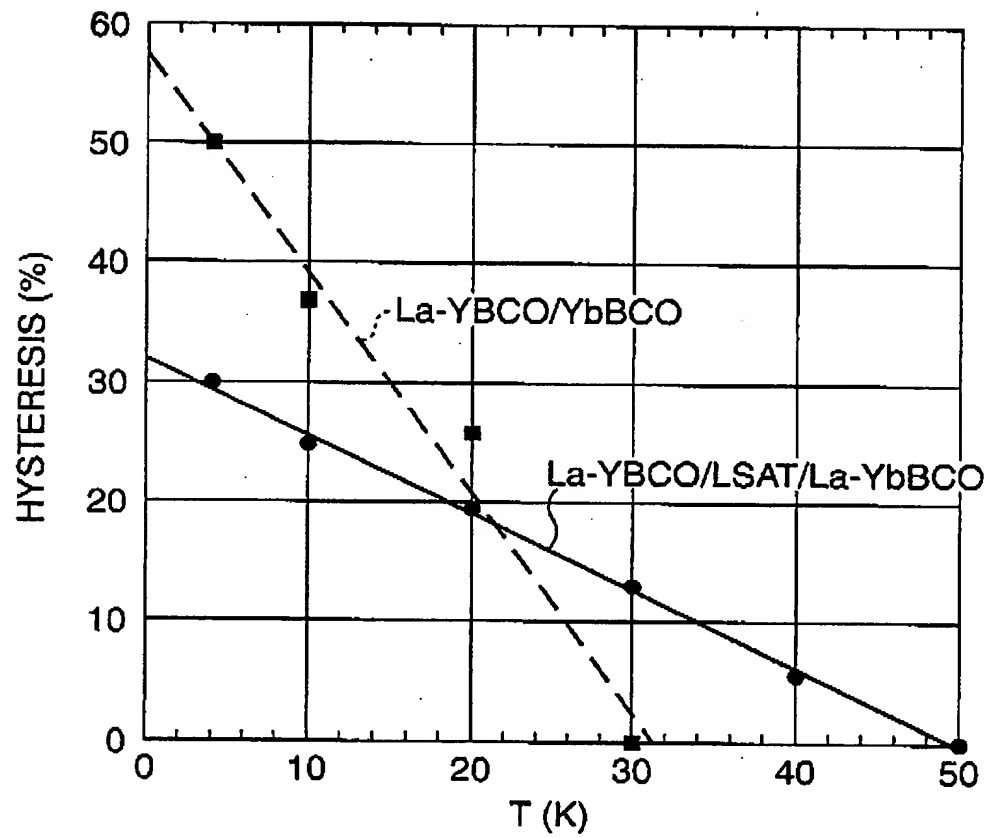


FIG. 2

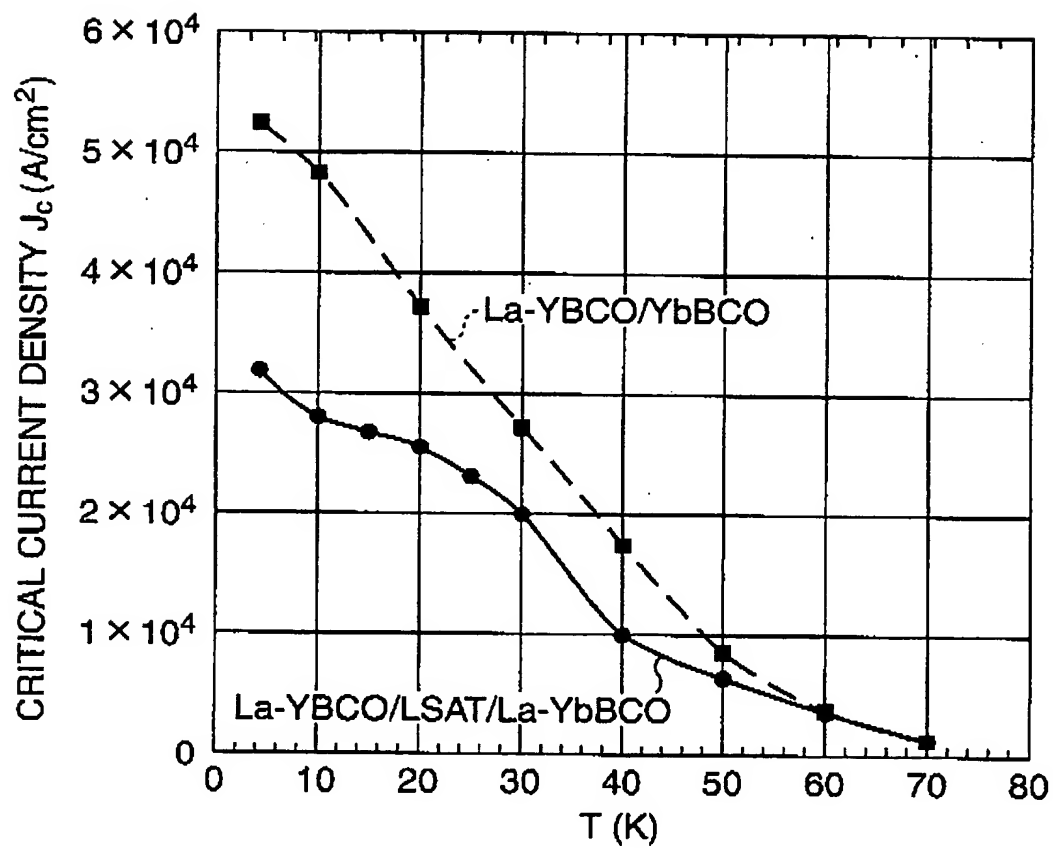
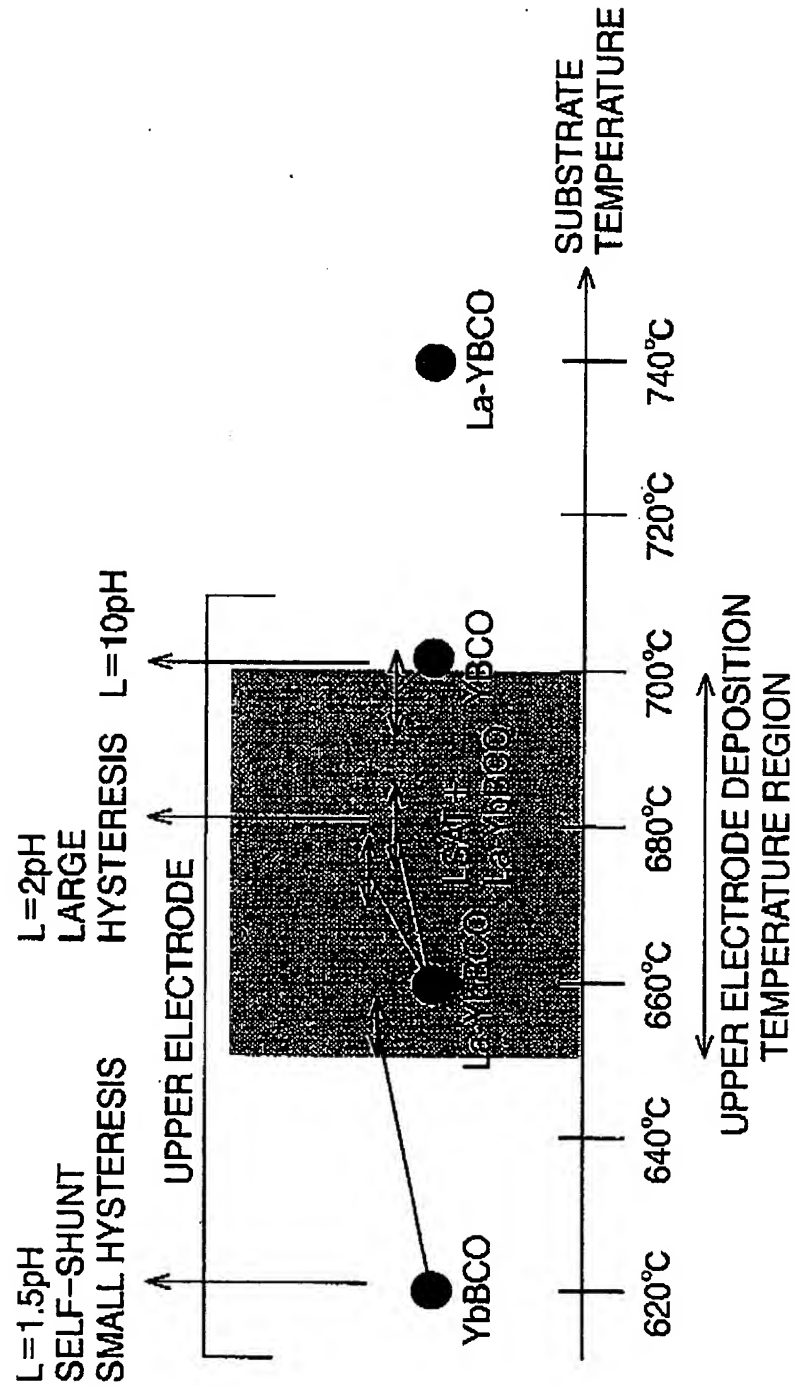


FIG. 3



<JUNCTION FOR SFQ CIRCUIT>

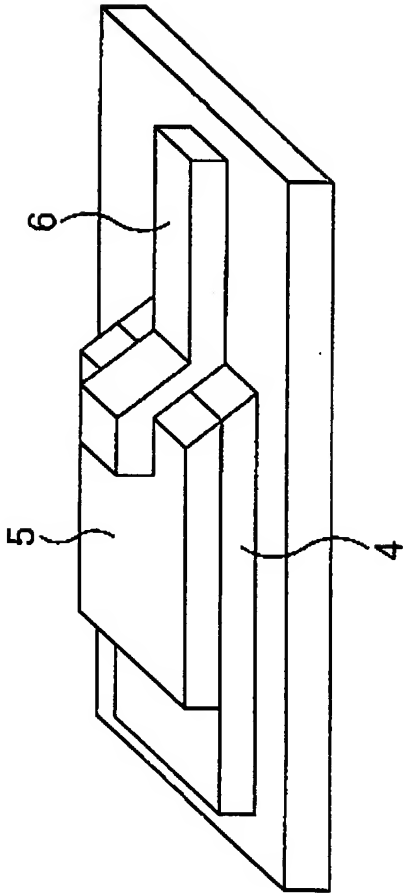


FIG. 4A

<JUNCTION FOR LATCH DRIVER CIRCUIT>

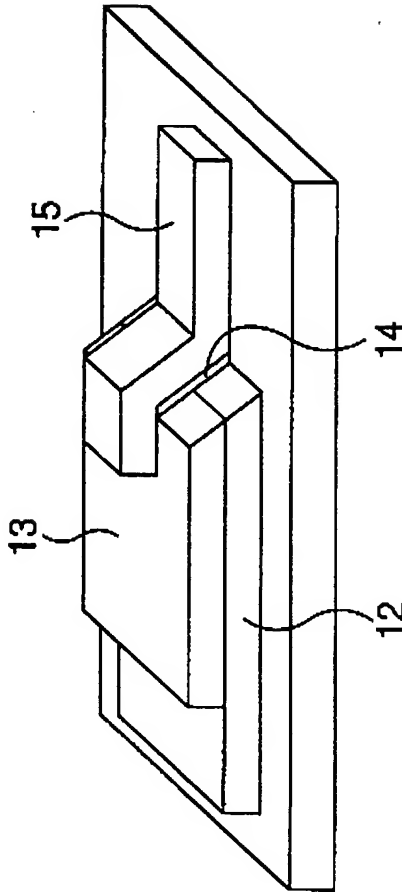


FIG. 4B

<JUNCTION FOR SFQ CIRCUIT>

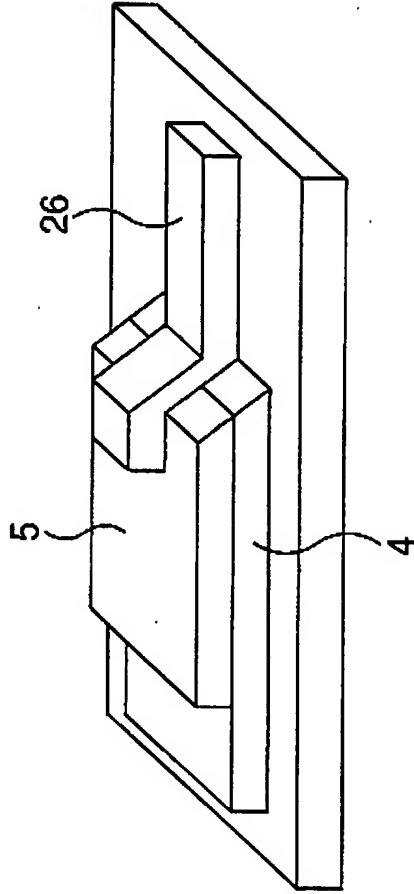


FIG. 5A

<JUNCTION FOR LATCH DRIVER CIRCUIT>

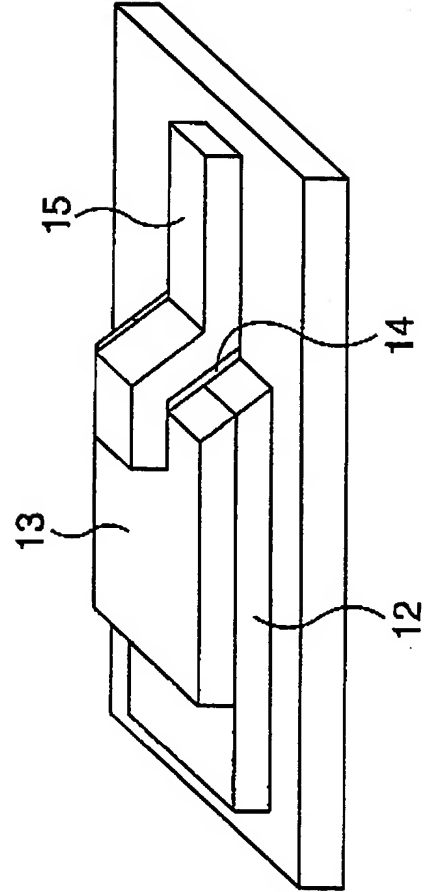


FIG. 5B

FIG. 6

<JUNCTION FOR SFQ CIRCUIT>

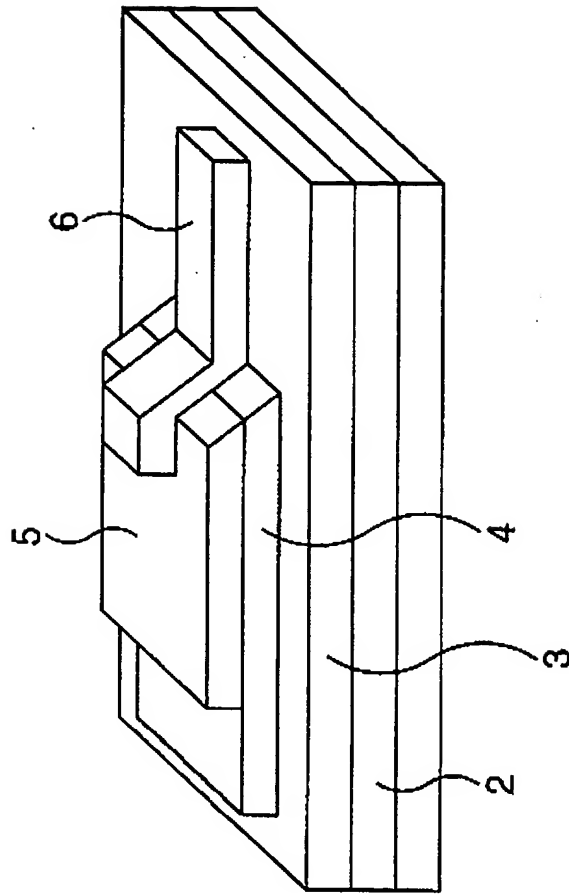


FIG. 7

<JUNCTION FOR SFQ CIRCUIT>

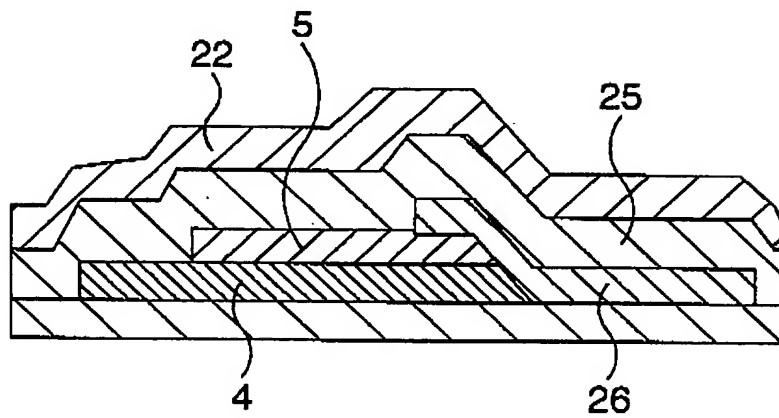


FIG. 8

<SFQ CIRCUIT>

<LATCH DRIVER CIRCUIT>

